



Phase II LAr Status (J. Parsons)

- Held Open Mtg for LAr Phase II on Monday
- Continuing to work to incorporate new groups
 - U Penn – preamp/shaper (collab. with BNL)
 - UT Dallas – ADC design (collab. with Columbia)
 - UT Austin – ASIC testing/irradiation (collab. with Columbia)
 - U Iowa – FCAL (collab. with U Az.) and/or HGTD
 - MSU – BE electronics (would like hardware design role)
- Decision on sFCAL will not be made until at least Dec. 2015
 - For now, keep options, but focus on costing, etc. for sFCAL (most expensive option)
- Situation with BE electronics is not very advanced
 - European interest in construction responsibility for sROD
 - Several US institutes (U Az, Oregon, Stony Brook, BNL, MSU) interested in participating
- High granularity timing detector (HGTD) is new topic, still very much in conceptual design phase
 - A few US institutes (UCSC, Oregon, BNL, Iowa) interested in participating